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The screenshot displays the HyperLynx - LineSim v8.2.1 software interface. The main workspace shows a schematic diagram of a USB D+ signal path. On the left, a Host component (CY7C68001_56SS... DPLUS) is connected to a Device component (C68013_128TQFP DPLUS) through a series of transmission lines (TL1 through TL12, TL19 through TL20, TL33 through TL37, and TL38) and resistors (R7 and R8). Each transmission line component displays its electrical characteristics, such as impedance (e.g., 49.7 ohms, 183.8 ohms) and propagation delay (e.g., 1.029 ns, 1.349 ns). The resistors R7 and R8 are labeled as 1.0 M ohms. An 'About' dialog box is open in the bottom right corner, providing information about the software version (v8.2 build 289387), operating system (Windows 7 V 6.1), memory status, and program location. The dialog box also includes a copyright notice for 2012 Mentor Graphics Corporation and an 'OK' button.

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